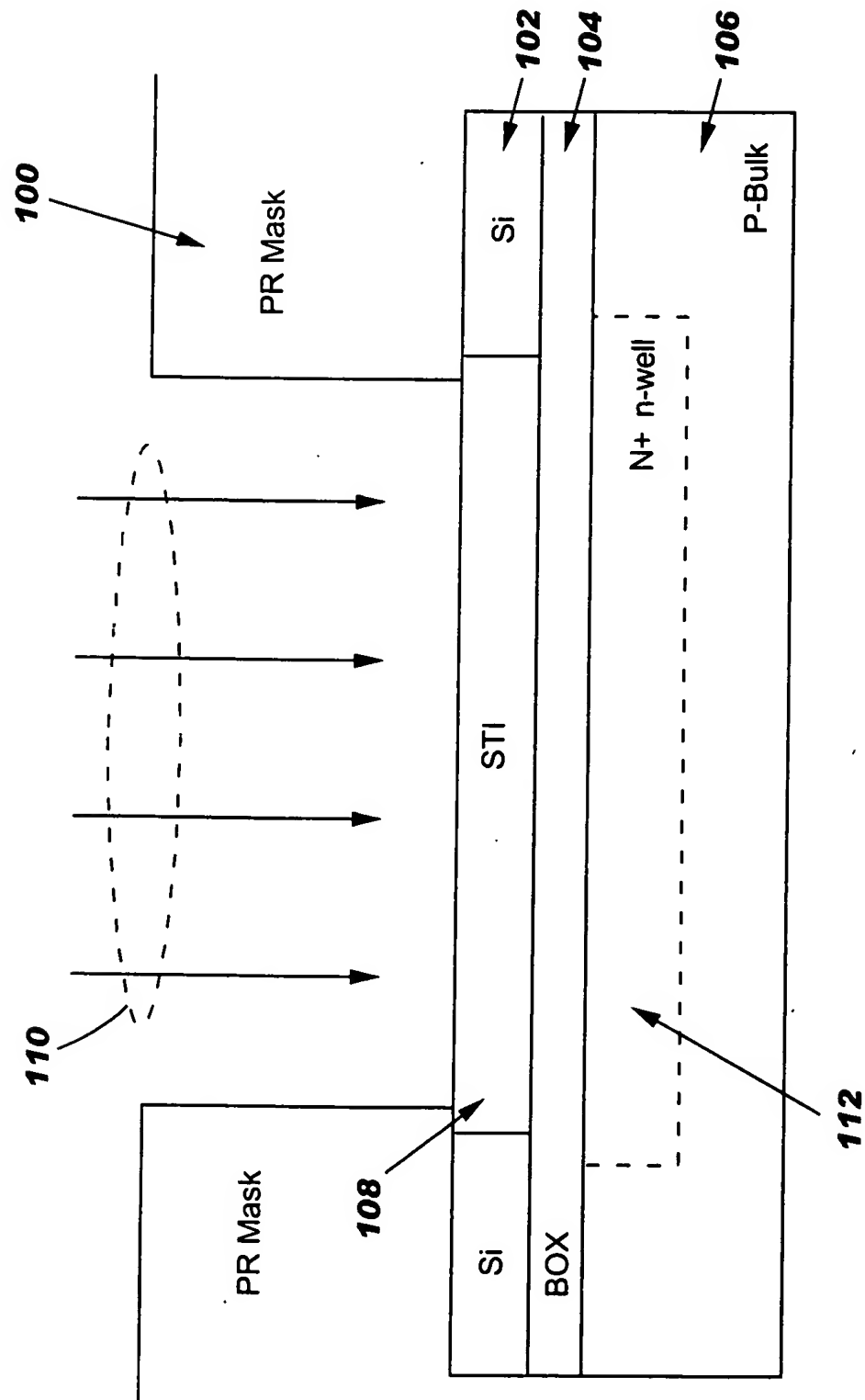


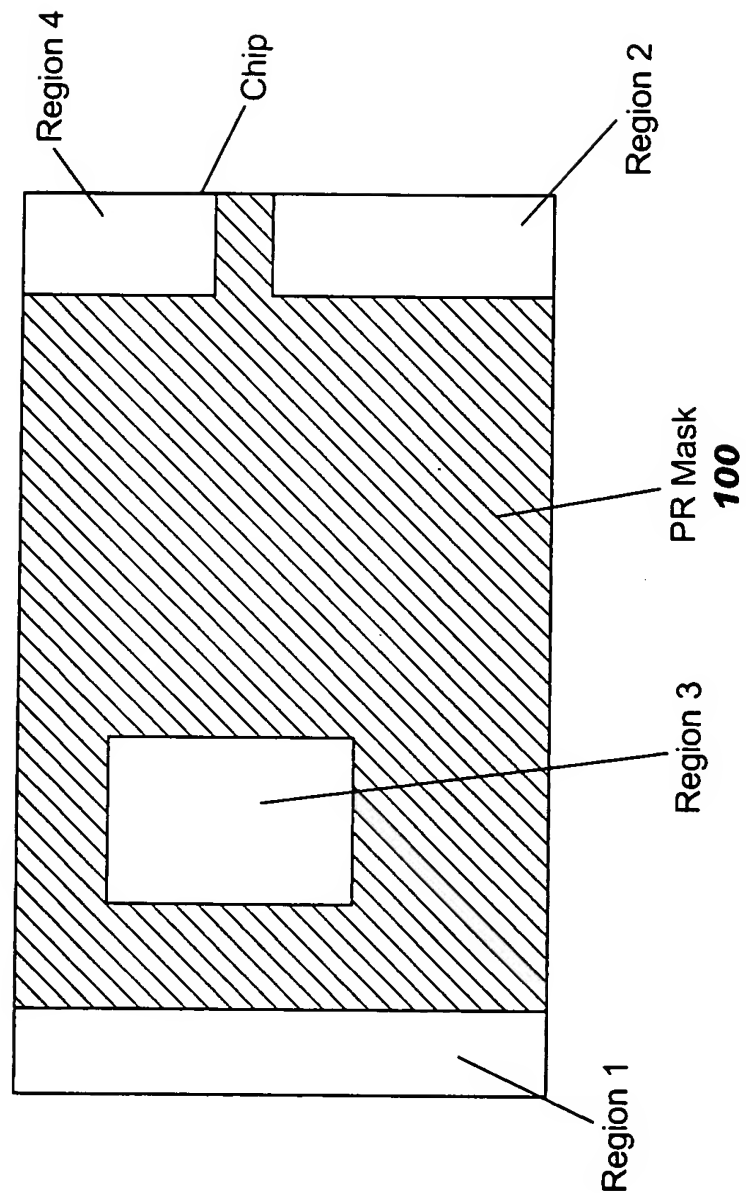


FIG. 1A



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FIG. 1B



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FIG. 2A

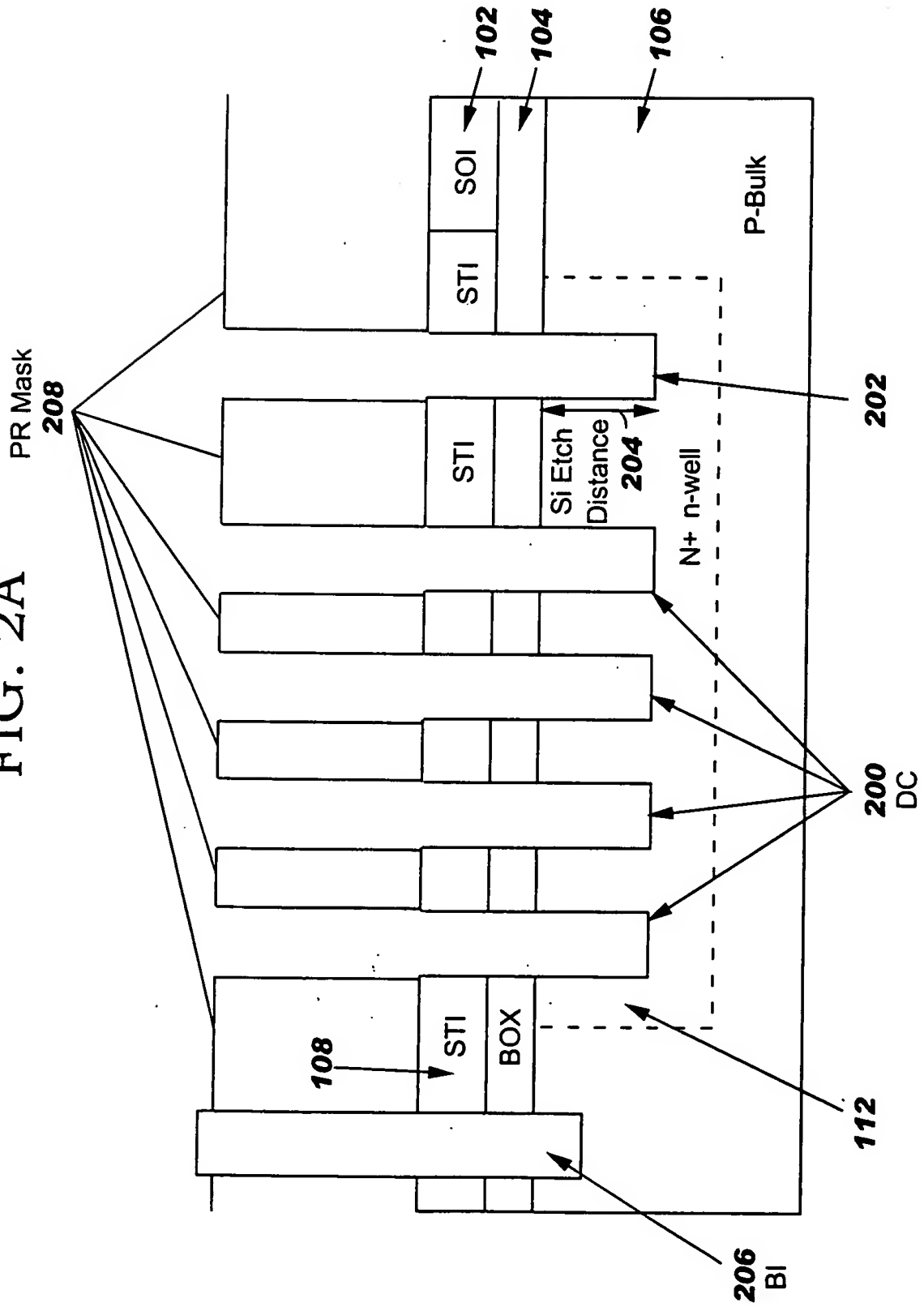


FIG. 2B

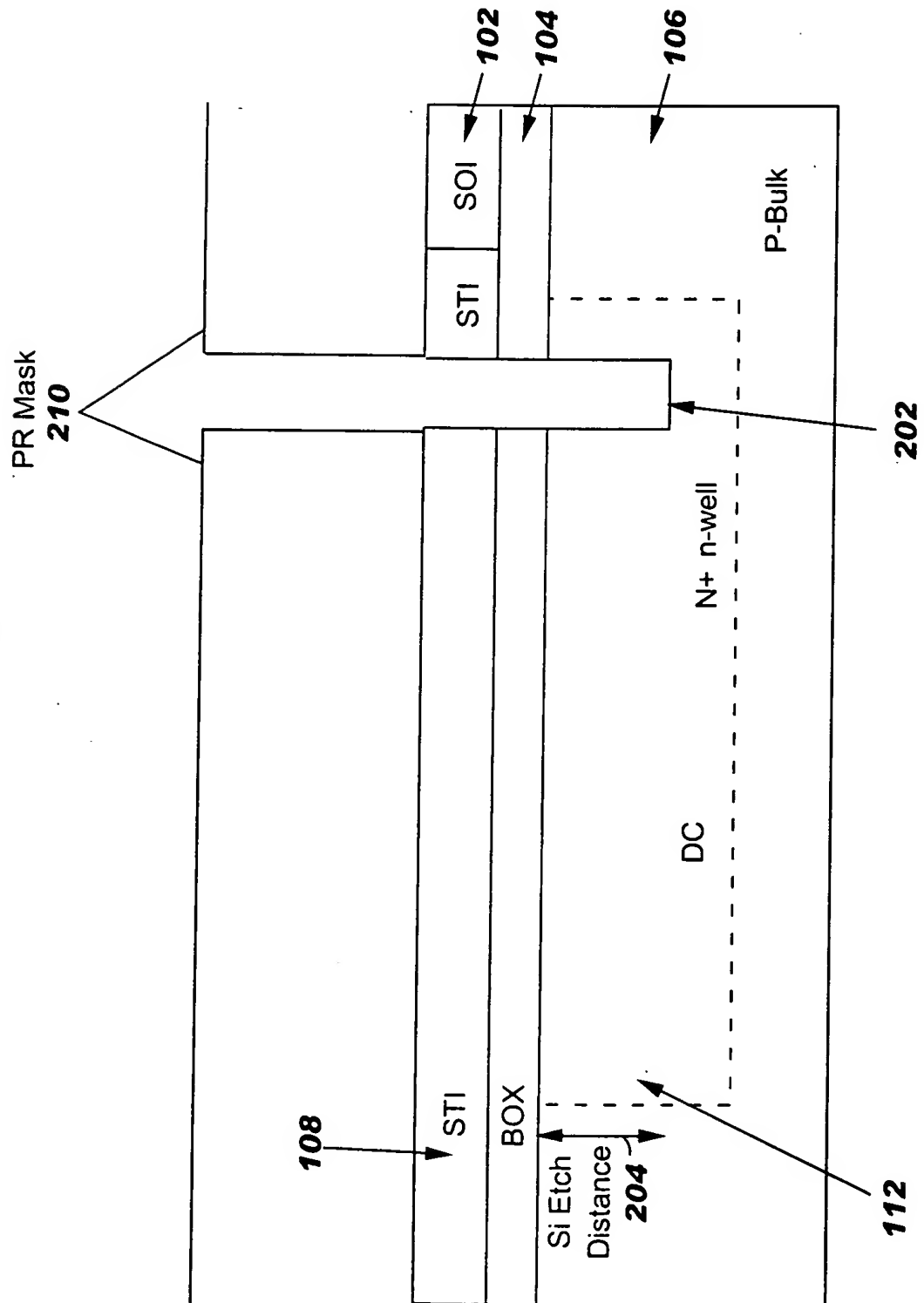


FIG. 2C

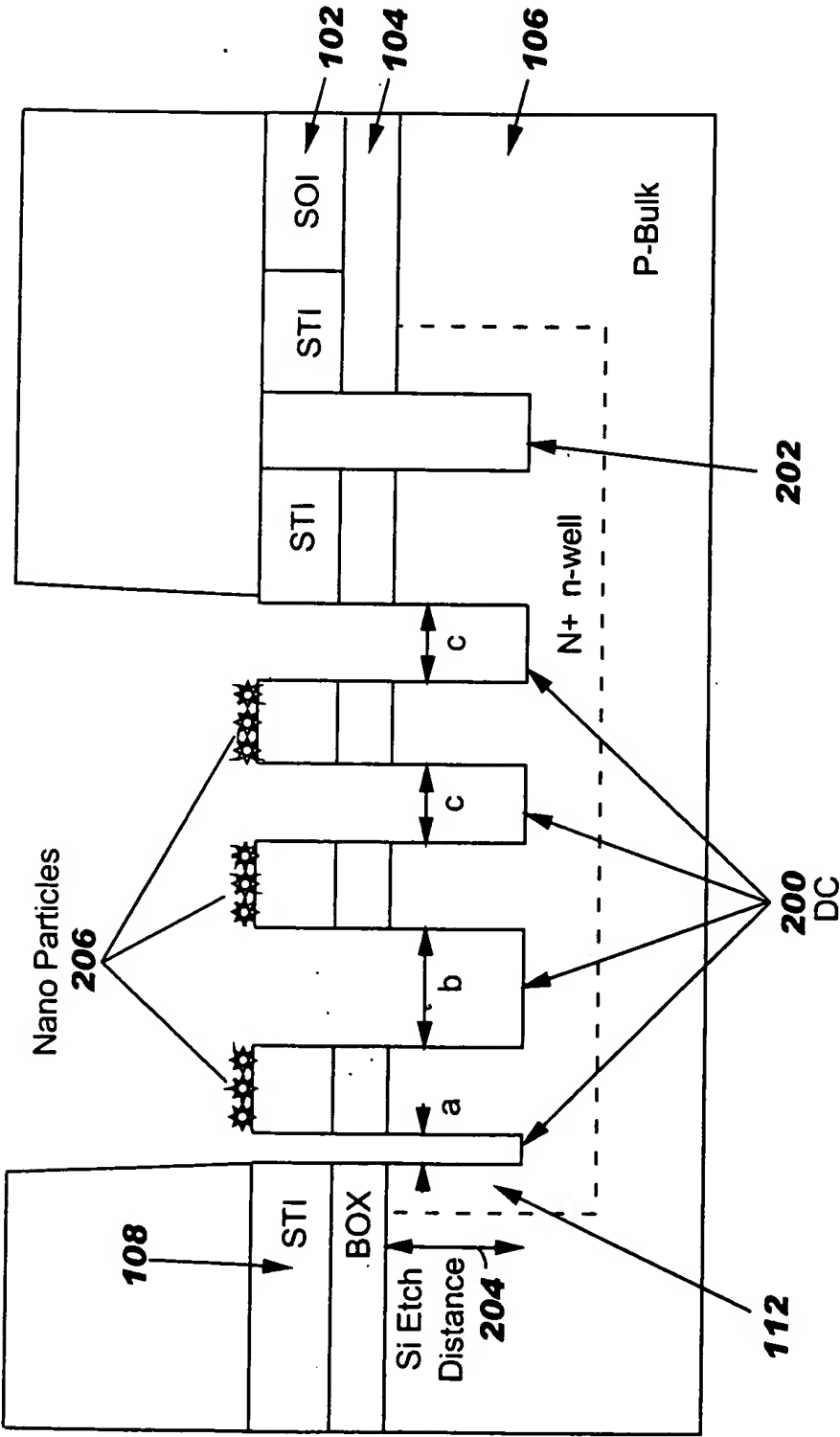
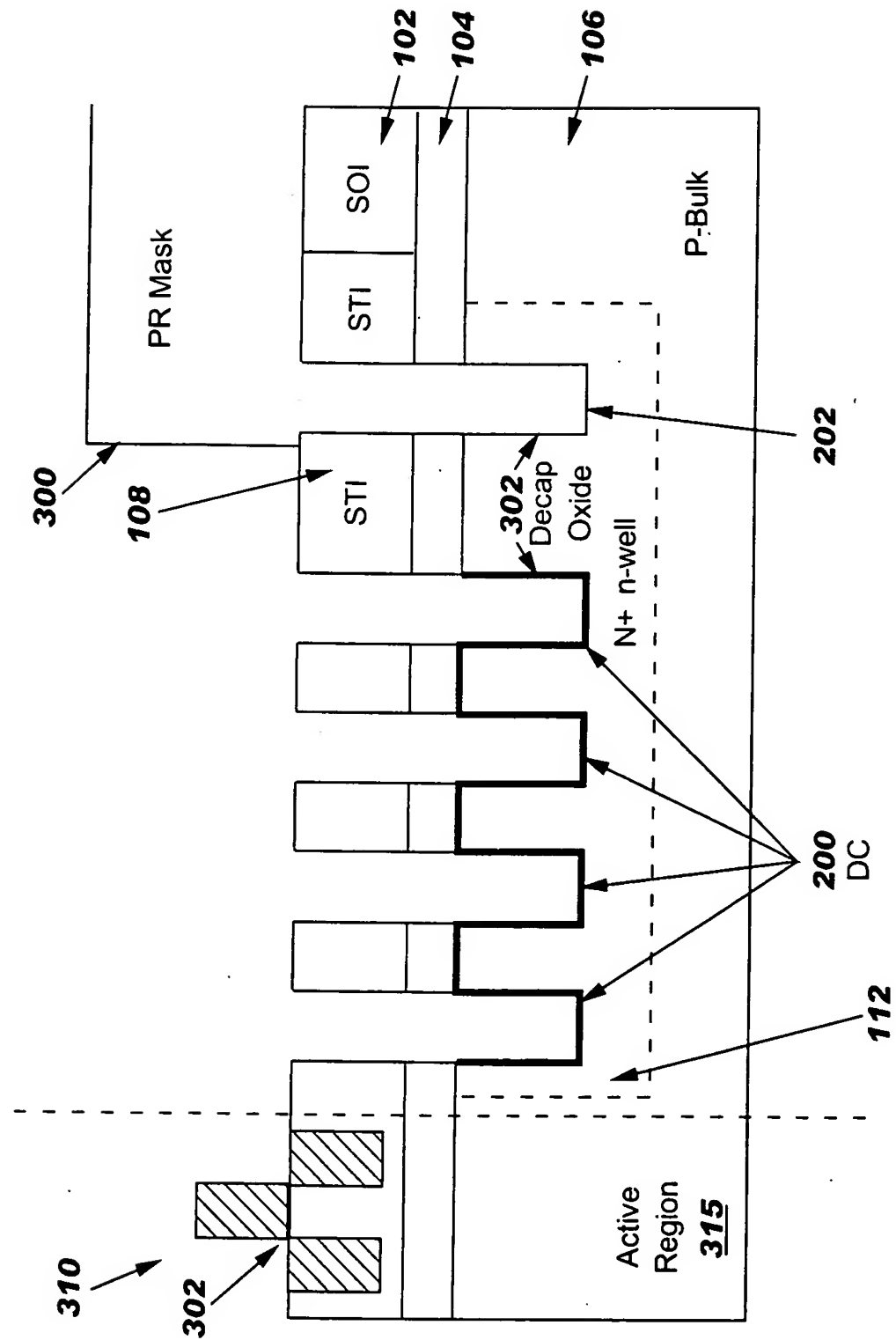
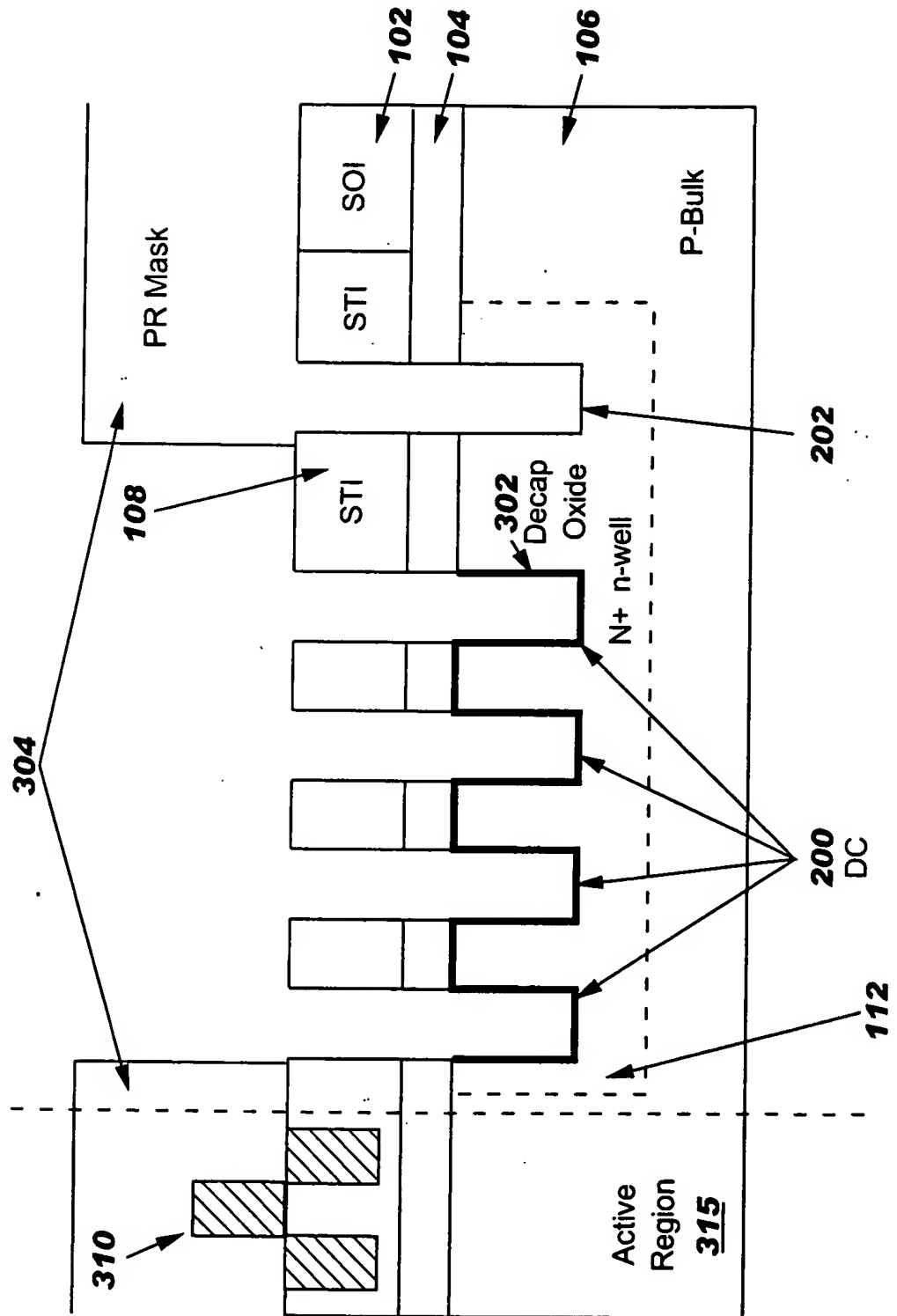


FIG. 3A



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FIG. 3B



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FIG. 3C

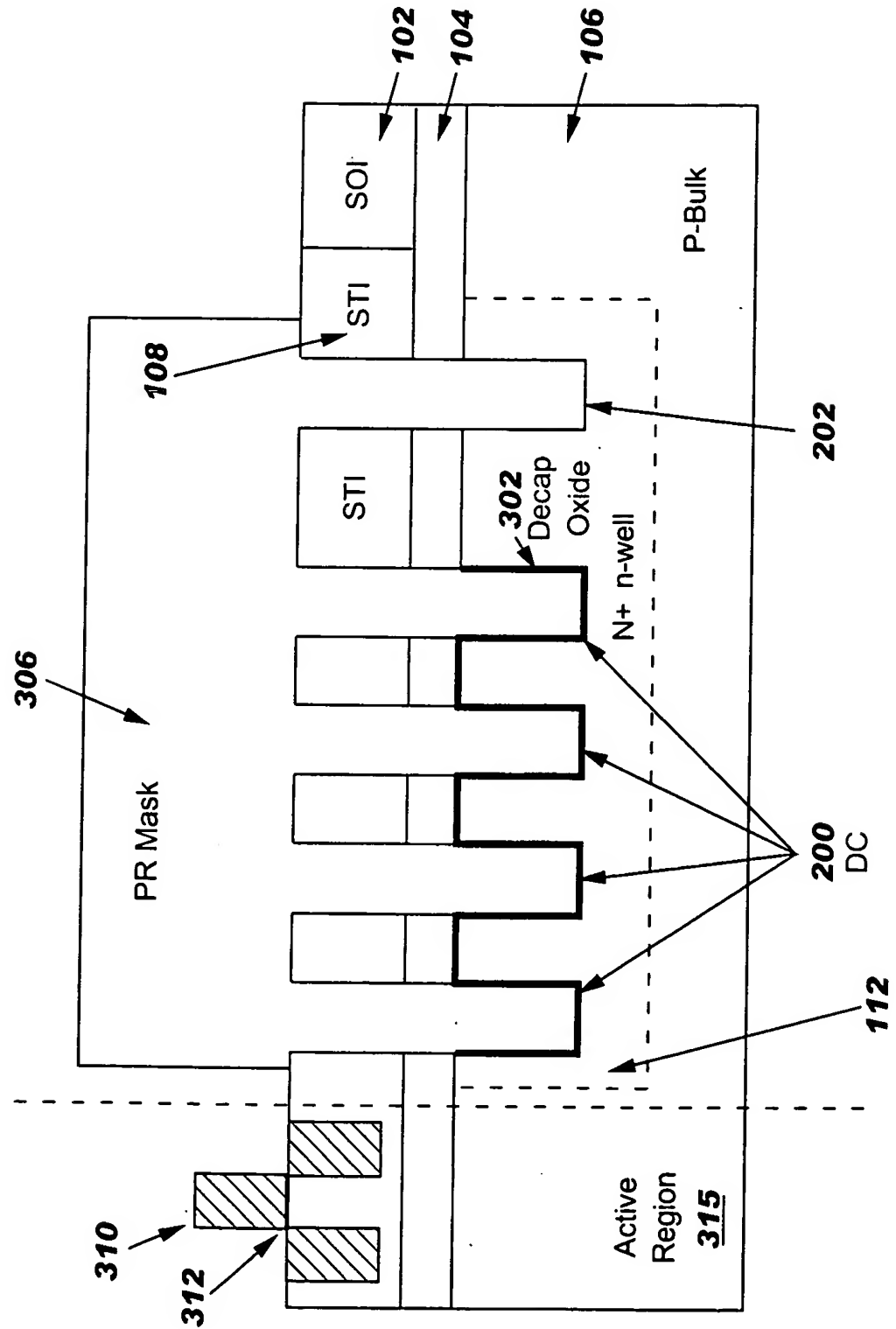
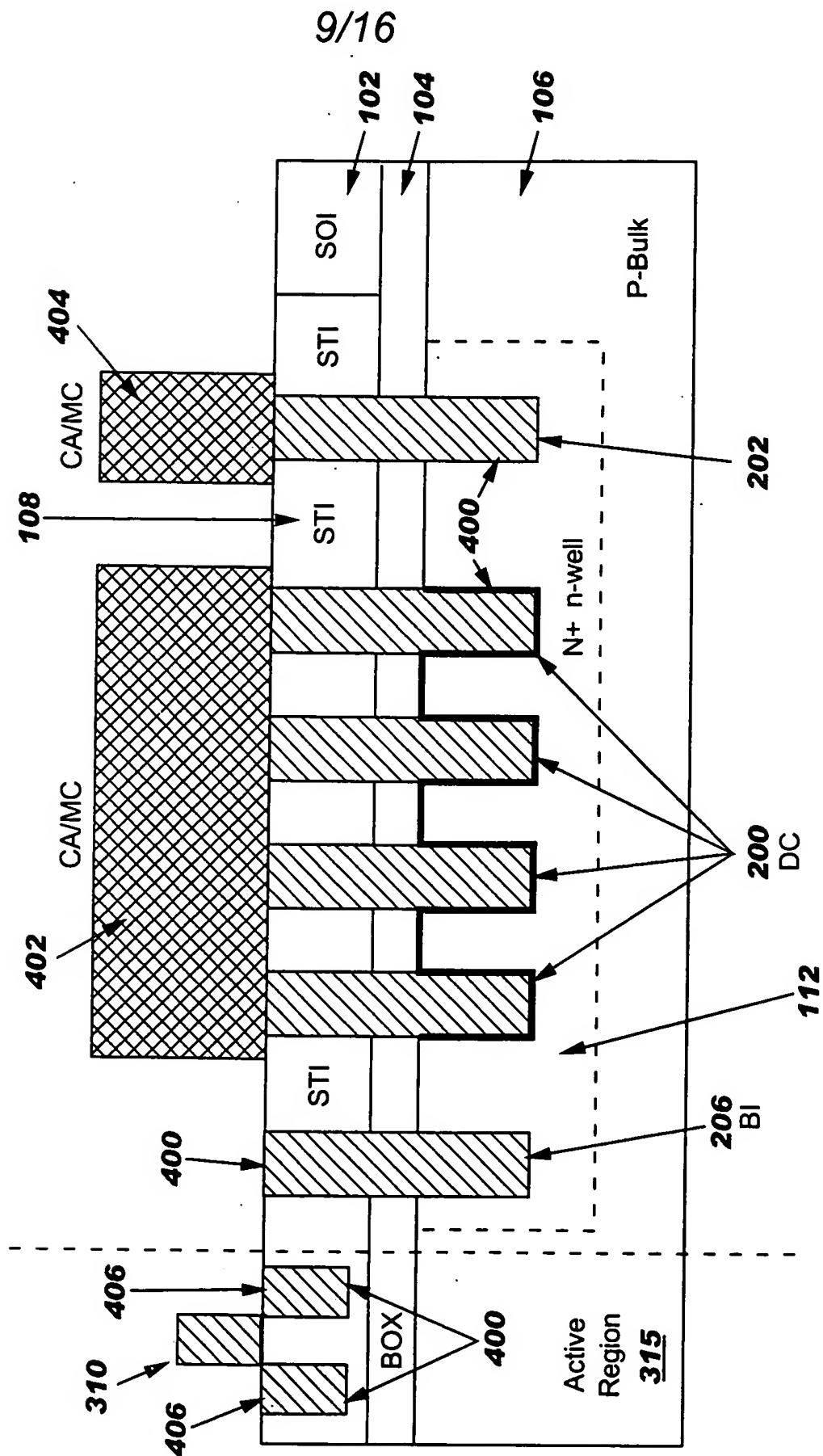
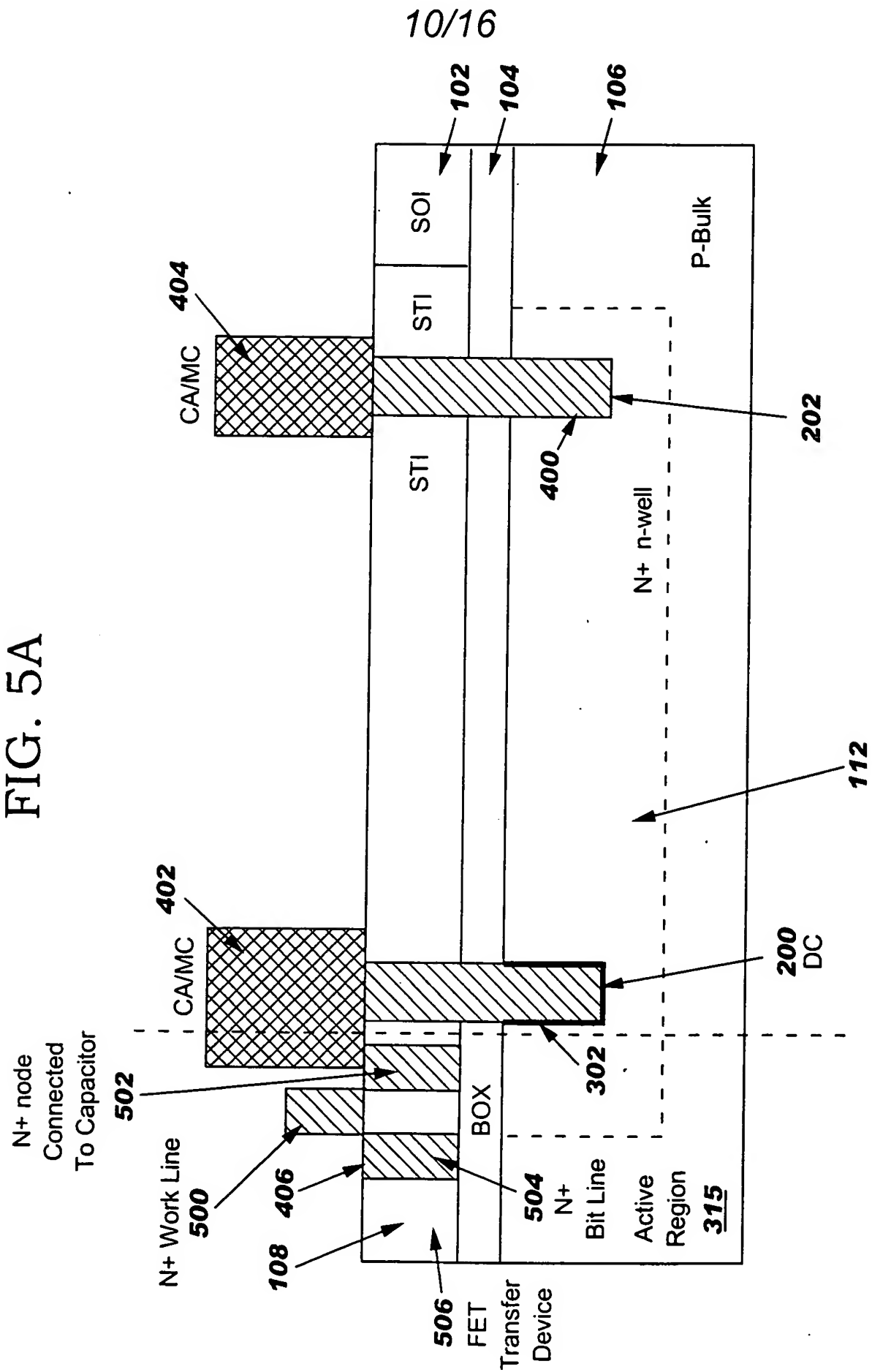




FIG. 4



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**N+ node  
Connected  
To Capacitor**

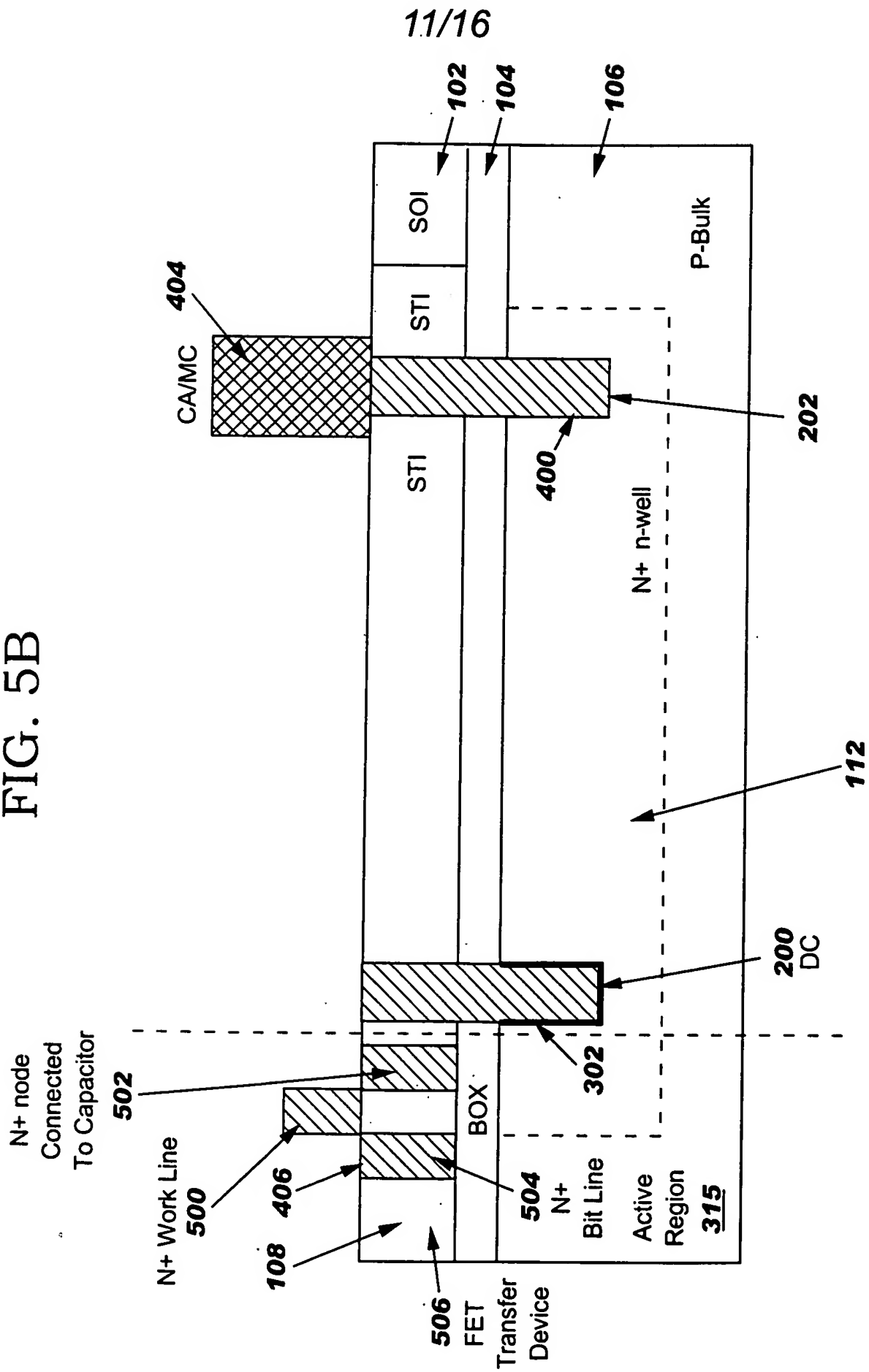
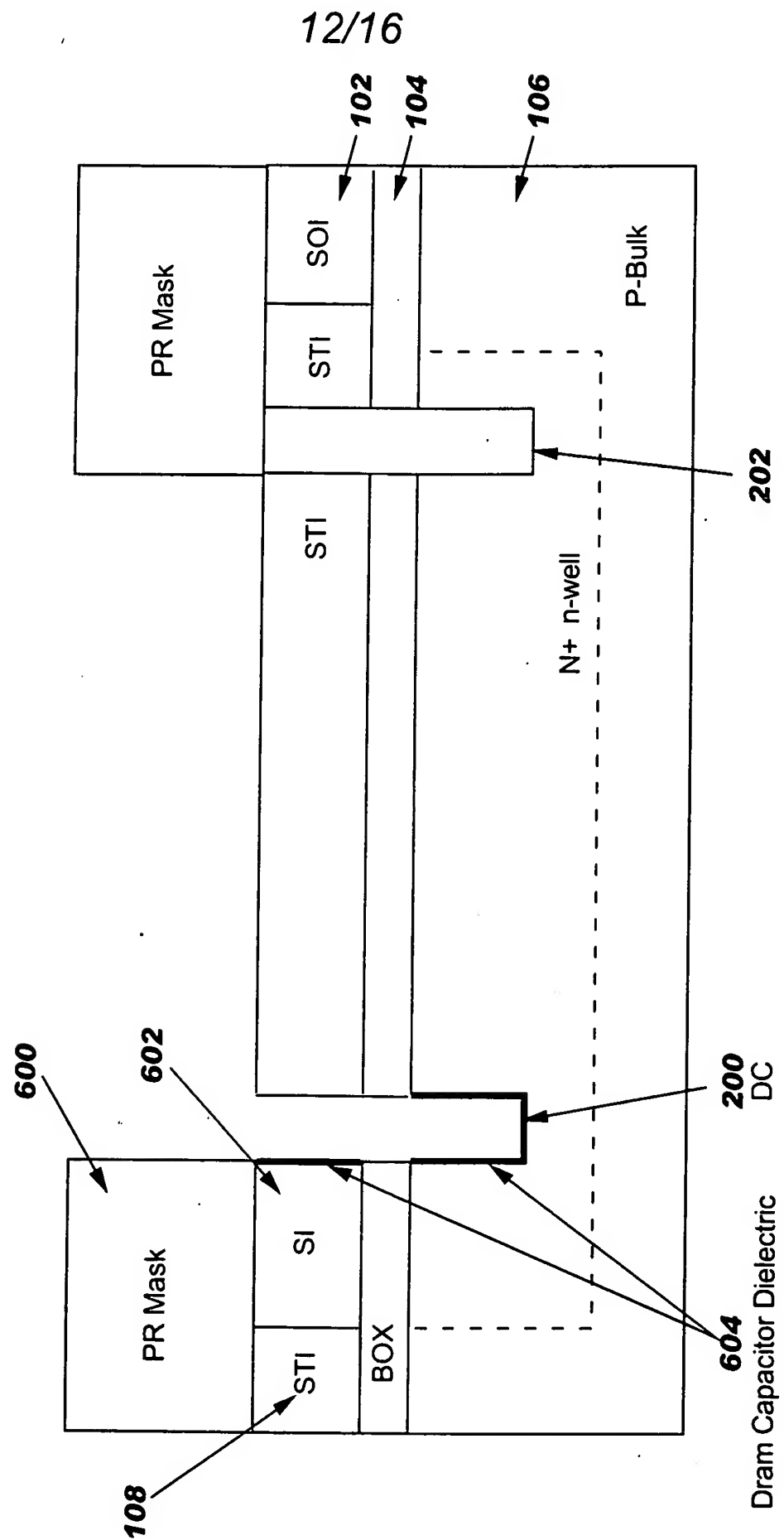


FIG. 6



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FIG. 7

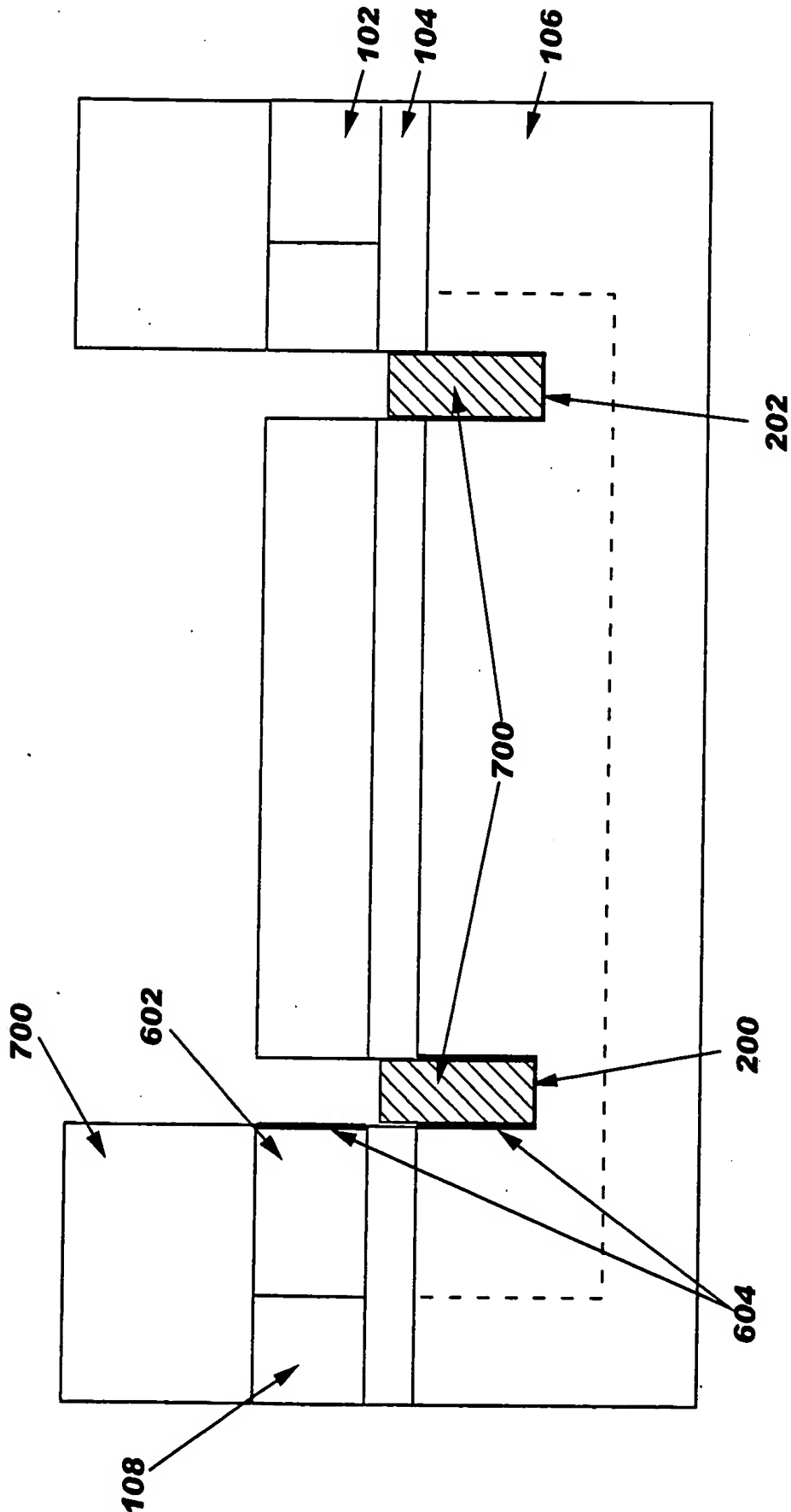


FIG. 8

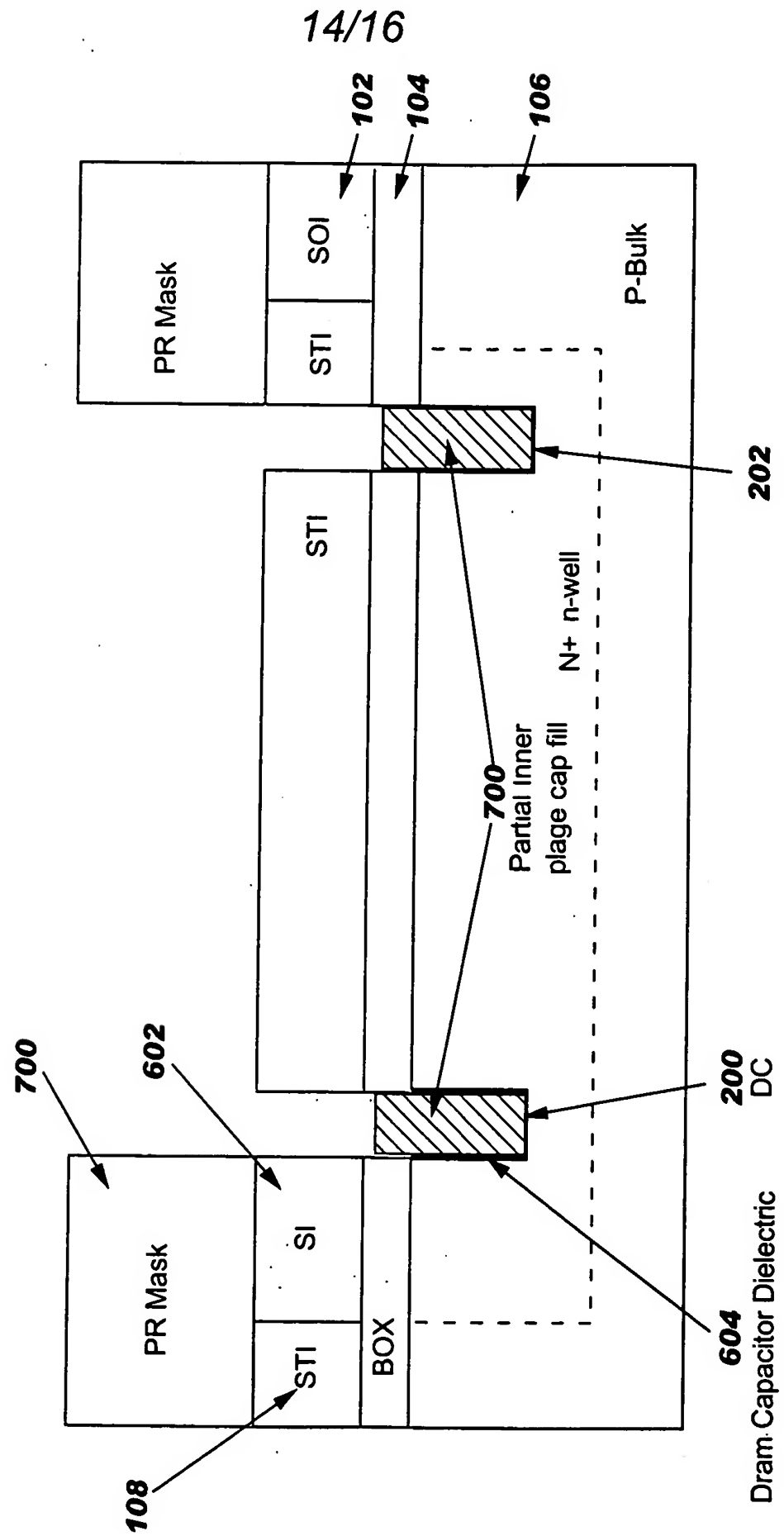


FIG. 9

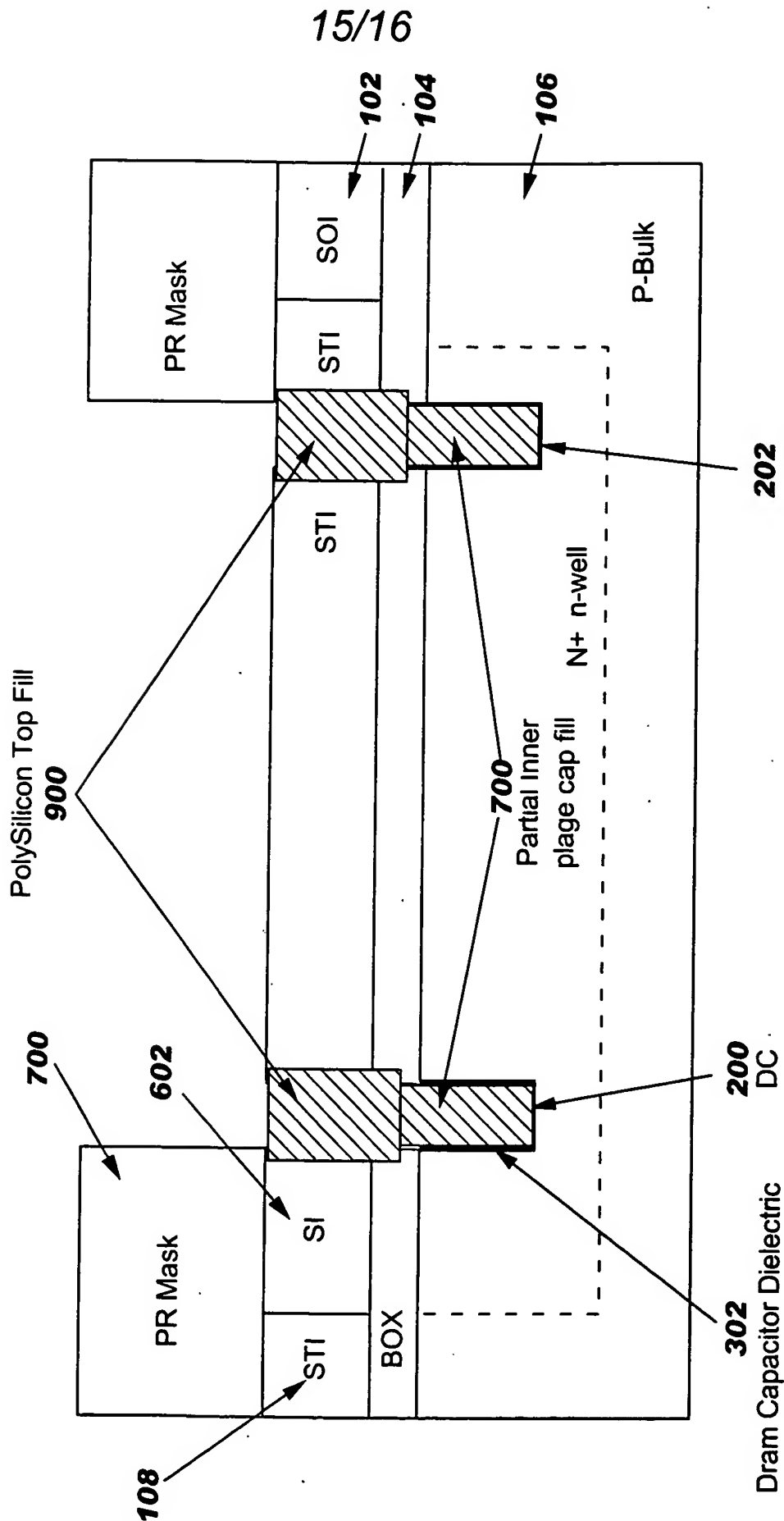


FIG. 10

